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CMOS INVERTER AND INTEGRATED CIRCUITS UTILIZING STRAINED

SILICON SURFACE CHANNEL MOSFETS

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

PRELIMINARY AMENDMENT

After assigning a filing date and serial number to this continuation application but before examining it, please amend this continuation application as follows.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 6 of this paper.